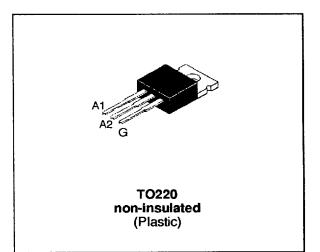


# T12xxxH

## STANDARD TRIACS

## FEATURES

- IT(RMS) = 12A
- VDRM = 400V to 800V
- High surge current capability



### DESCRIPTION

The T12xxxH series of triacs uses a high performance MESA GLASS technology. These parts are intended for general purpose switching and phase control applications.

#### **ABSOLUTE RATINGS** (limiting values)

Symbol	Parameter	Value	Unit		
IT(RMS)	RMS on-state current (360° conduction angle)	Tc= 90 °C	12	A	
I <sub>TSM</sub>	Non repetitive surge peak on-state current	tp = 8.3 ms	115	A	
	(T <sub>j</sub> initial = 25°C)	tp = 10 ms	110		
l <sup>2</sup> t	I <sup>2</sup> t Value for fusing	tp = 10 ms	60	A <sup>2</sup> s	
dl/dt	Critical rate of rise of on-state current $k_{B} = 500 \text{ mA}$ $di_{G}/dt = 1 \text{ A}/\mu \text{s}.$	Repetitive F = 50 Hz	10	A/µs	
		Non Repetitive	50		
T <sub>stg</sub> Tj	Storage and operating junction temperature	range	- 40, +150 - 40, +125	°C	
TI	Maximum lead temperature for soldering du 4.5mm from case	ring 10s at	260	°C	

Symbol	Parameter	Voltage				Unit
		D	М	S	Ν	
Vdrm Vrrm	Repetitive peak off-state voltage $T_j = 125^{\circ}C$	400	600	700	800	V

#### January 1995

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## T12xxxH

### THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
Rth(j-a)	Junction to ambient	60	°C/W
Rth(j-c)	Junction to case for D.C	3.3	°C/W
Rth(j-c)	Junction to case for A.C 360° conduction angle (F=50Hz)	2.5	°C/W

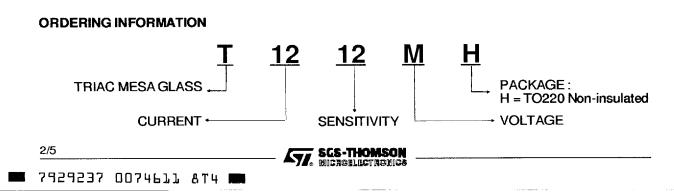
GATE CHARACTERISTICS (maximum values)

 $P_{G (AV)} = 1 W P_{GM} = 10 W (tp = 20 \ \mu s)$   $I_{GM} = 4 A (tp = 20 \ \mu s)$ 

## **ELECTRICAL CHARACTERISTICS**

Symbol	Test Conditions		Quadrant		Sensitivity			Unit
					10	12	13	
lат	V <sub>D</sub> =12V (DC) R <sub>L</sub> =33Ω	Tj= 25°C	1-11-111	MAX	25	50	50	mA
			IV	MAX	25	50	75	
V <sub>GT</sub>	V <sub>D</sub> =12V (DC) R <sub>L</sub> =33Ω	Tj= 25°C	I-11-111-1V	MAX		1.5		V
V <sub>GD</sub>	Vd=Vdrm RL=3.3kΩ	Tj= 125℃	1-11-111-IV	MIN		0.2		ν
tgt	V <sub>D</sub> =V <sub>DRM</sub> k <sub>G</sub> = 500mA k <sub>T</sub> = 17A dl <sub>G</sub> /dt = 3A/μs	Tj= 25°C	1-11-111-1V	ТҮР		2		μs
<b>і</b> н *	h= 250 mA Gate open	Tj= 25°C		MAX	25	50	75	mA
١L	IG= 1.2 IGT	Tj= 25°C	I-111-1V	TYP	25	50	75	mA
			11	ТҮР	50	100	150	
V <sub>TM</sub> *	I <sub>TM</sub> = 17Α tp= 380μs	Tj= 25°C		MAX	1.5			V
DRM	VD = VDRM			MAX	10			μA
IRRM	V <sub>R</sub> = V <sub>RRM</sub> Tj= 110°	Tj= 110℃		MAX		2		mA
dV/dt*	VD=67%V <sub>DRM</sub> Gate open	Tj= 110℃		MIN	200	500	500	V/µs
(dV/dt)c*	(dl/dt)c = 5.3 A/ms	Tj= 110°C		MIN	2	5	10	V/µs

\* For either polarity of electrode A2 voltage with reference to electrode A1



Be sure to visit ChipDocs site for more information http://www.chipdocs.com

Fig.1 : Maximum RMS power dissipation versus RMS on-state current.

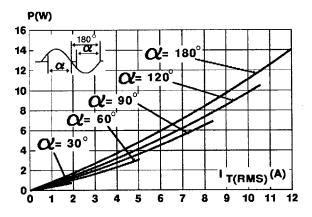


Fig.3: RMS on-state current versus case temperature.

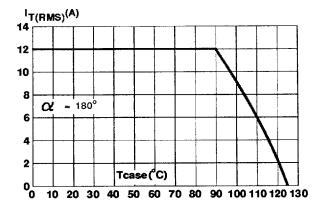
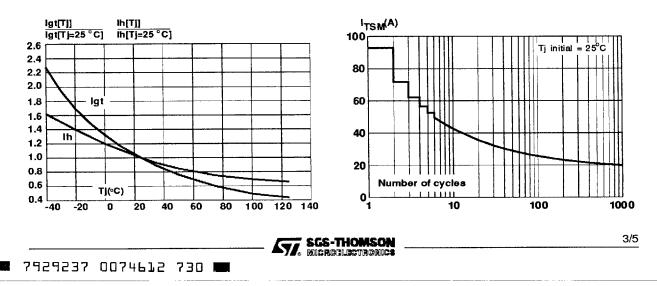


Fig.5: Relative variation of gate trigger current and holding current versus junction temperature.



**Fig.2**: Correlation between maximum RMS power dissipation and maximum allowable temperature (Tamb and Tcase) for different thermal resistances heatsink + contact.

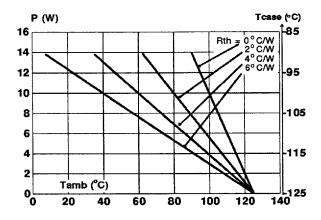


Fig.4 : Relative variation of thermal impedance versus pulse duration.

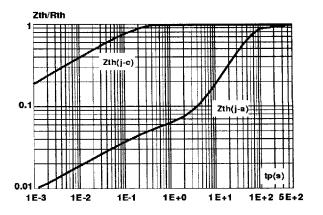
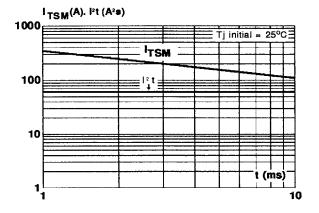


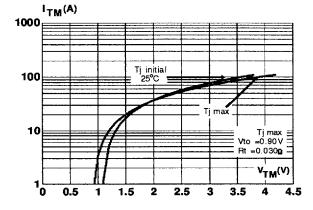
Fig.6 : Non repetitive surge peak on-state current versus number of cycles.

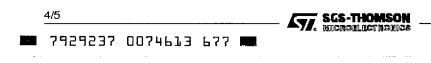
## T12xxxH

Fig.7: Non repetitive surge peak on-state current for a sinusoidal pulse with width :  $t \le 10$ ms, and corresponding value of ft.

Fig.8: On-state characteristics (maximum values).

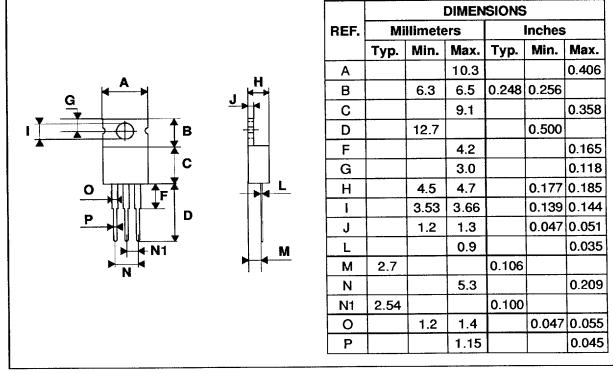






## PACKAGE MECHANICAL DATA

TO220 Non-insulated (Plastic)



Marking : type number Weight : 1.8 g

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